PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	:	Venkatapathi R. Nallapa
Application No.	:	10/658,124
Filed	:	September 9, 2003
For	:	EEPROM EMULATION IN FLASH MEMORY

Art Unit	:	2818
Docket No.	:	130209.511
Date	:	December 18, 2003

Mail Stop Missing Parts
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents:

In accordance with 37 C.F.R. §§ 1.56 and 1.97 through 1.98, applicant wishes to make known to the U.S. Patent and Trademark Office the references set forth on the attached form PTO-1449 (copies of the cited references are enclosed). As to any reference supplied, applicant does not admit that it is "prior art" under 35 U.S.C. §§ 102 or 103, and specifically reserves the right to traverse or antedate any such reference, as by a showing under 37 C.F.R. § 1.131 or other method. Although the aforesaid references are made known to the Patent and Trademark Office in compliance with applicant's duty to disclose all information he is aware of which is believed relevant to the examination of the above-identified application, applicant believes that his invention is patentable.

Please acknowledge receipt of this Information Disclosure Statement and kindly make the cited references of record in the above-identified application.

Applicant believes this Information Disclosure Statement has been timely filed, however, the Director is authorized to charge any fee due by way of this Information Disclosure Statement to our Deposit Account No. 19-1090.

Respectfully submitted,

Seed Intellectual Property Law Group PLLC

Frank Abramonte
Registration No. 38,066

Enclosures:
      Form PTO-1449
      Cited References (4)

701 Fifth Avenue, Suite 6300
Seattle, Washington 98104-7092
Phone: (206) 622-4900
Fax: (206) 682-6031

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FORM PTO-1449 (REV.7-80)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	ATTY. DOCKET NO. 130209.511	APPLICATION NO. 10/658,124
		APPLICANT Venkatapathi R. Nallapa	
		FILING DATE September 9, 2003	GROUP ART UNIT 2818

DEC 1 8 2003

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION	
					YES	NO
	AH					
	AI					
	AJ					
	AK					
	AL					

OTHER PRIOR ART *(Including Author, Title, Date, Pertinent Pages, Etc.)*

	AM	"EEPROM Replacement with Flash Memory – Intel Application Note AP-685," Intel Corporation, Denver, CO, December 1998, pp 1-20.
	AN	Hazen, P., "Using Intel's Boot Block Flash Memory Parameter Blocks to Replace Eeprom - Intel Application Note AP-604," Intel Corporation, Denver, CO, November 1995, pp 1-6.
	AO	"Am29F200B Data Sheet," Advanced Micro Devices (AMD), Publication # 21526, Rev: C Amendment/+2, Sunnyvale, CA, November 1999, pp 1-39.
	AP	STMicroelectonics, "Flash+, Exit the Emulation Compromise," ST Challenge, 4th Edition, 1997, retrieved from http://us.st.com/stonline/press/magazine/challeng/4thedit/chal5.htm.

EXAMINER	DATE CONSIDERED

* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).

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Date: 12/18/03